

FIG. 1

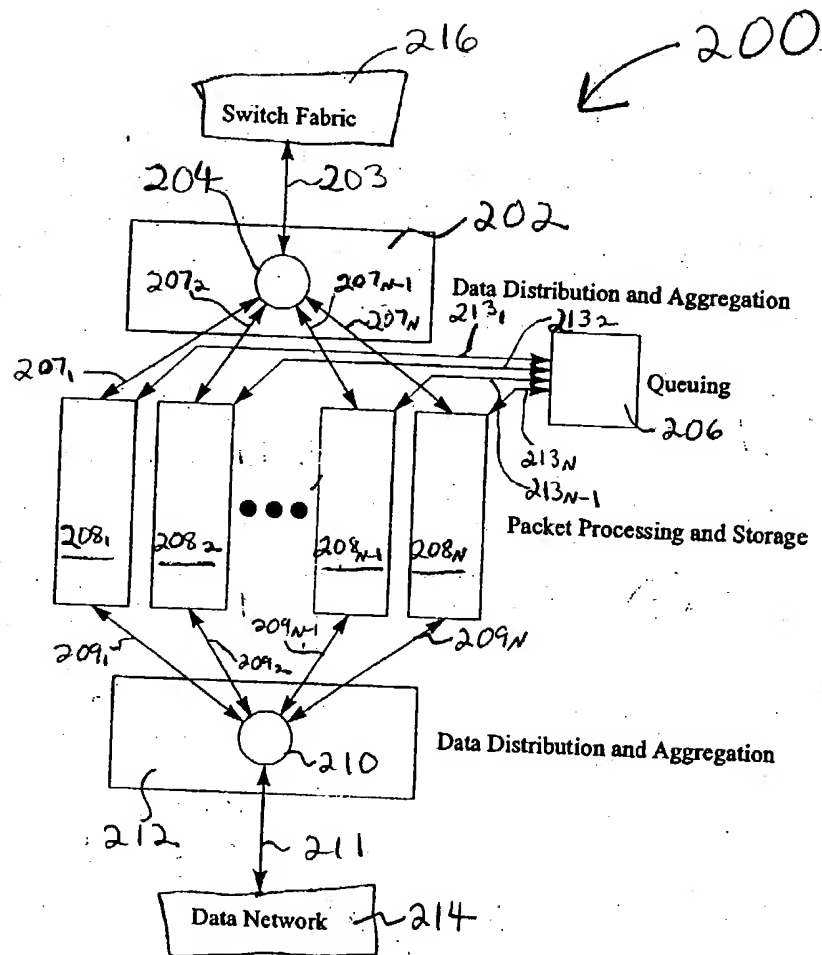


FIG. 2

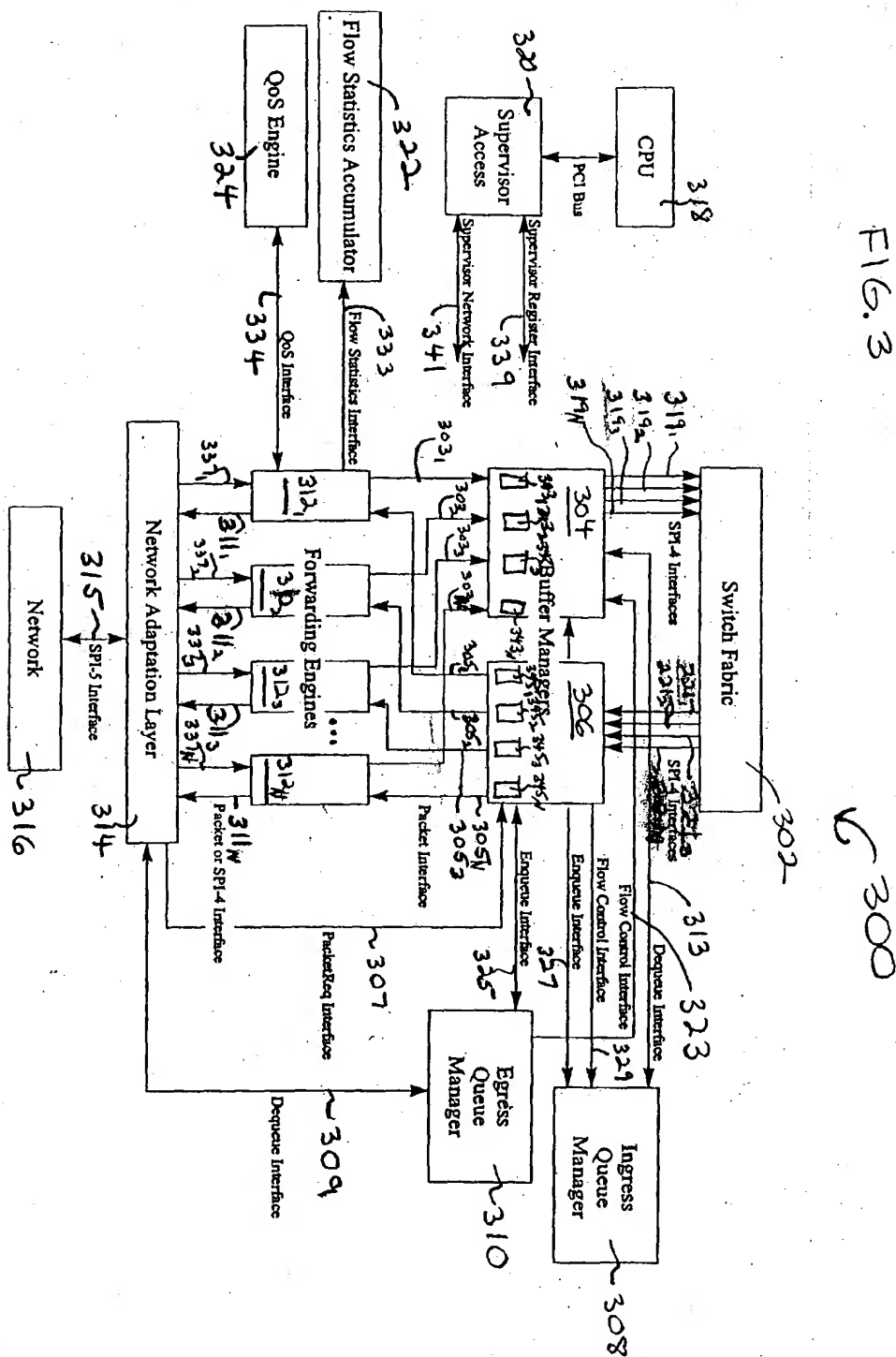


FIG. 4A

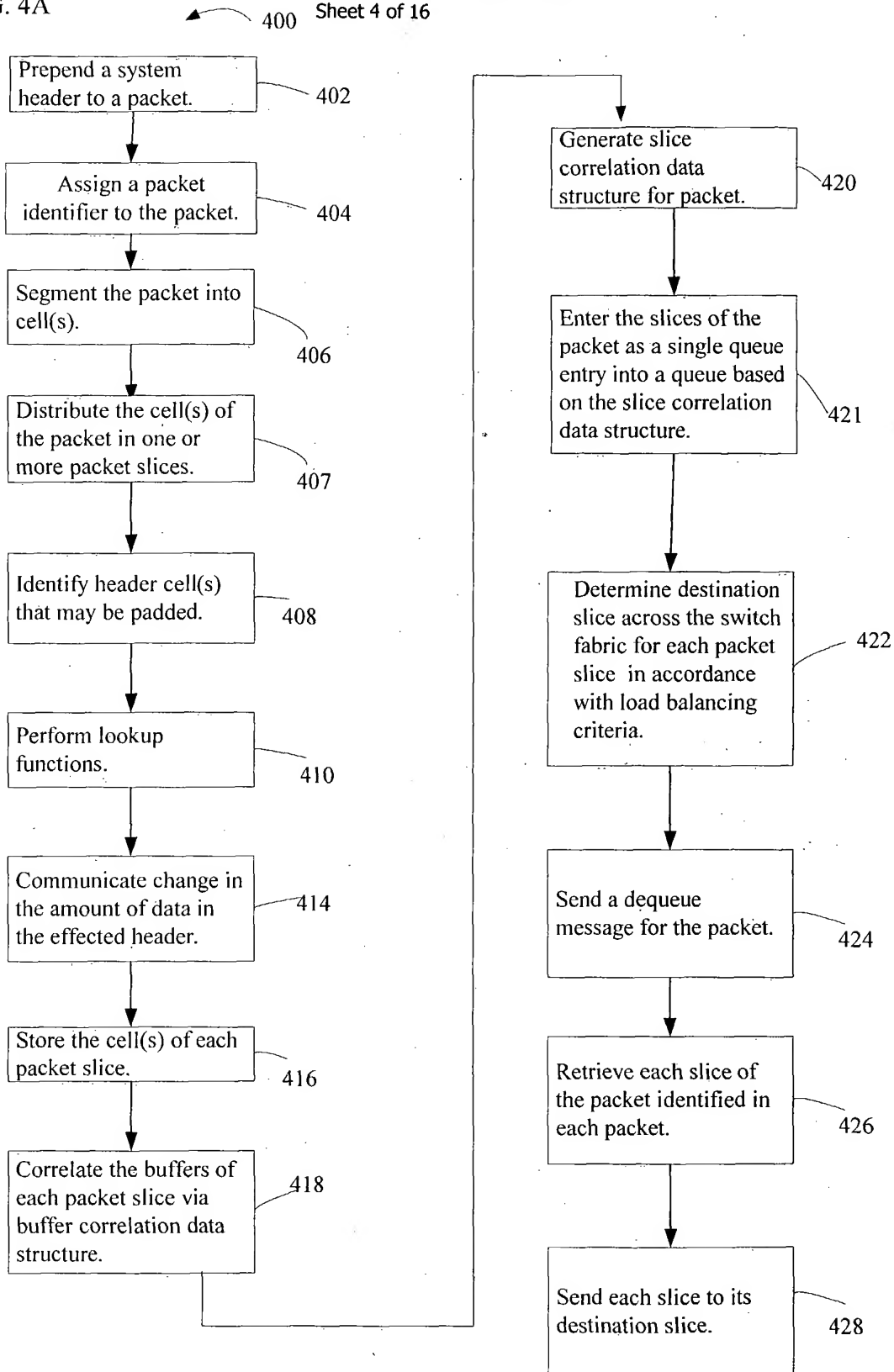
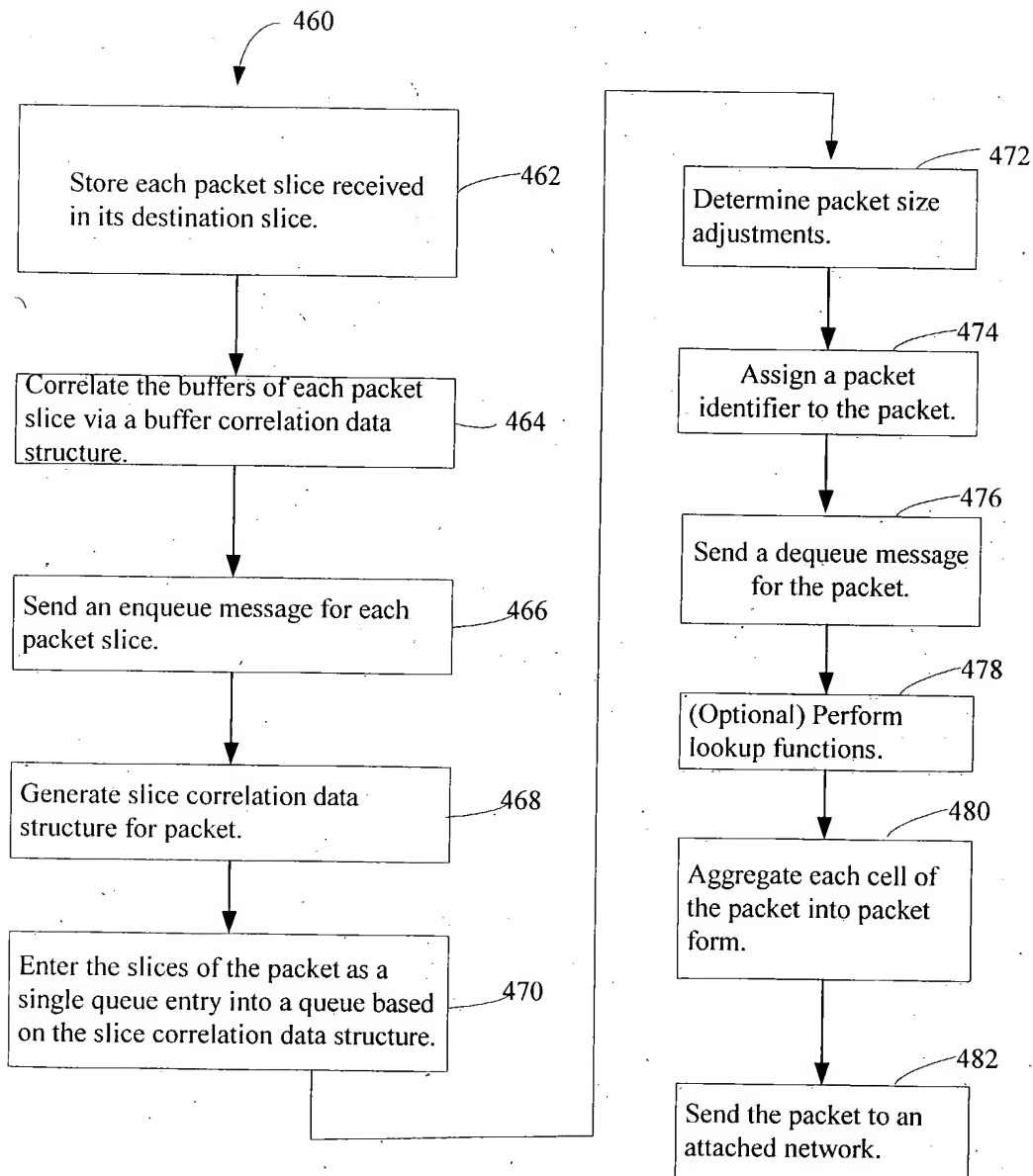
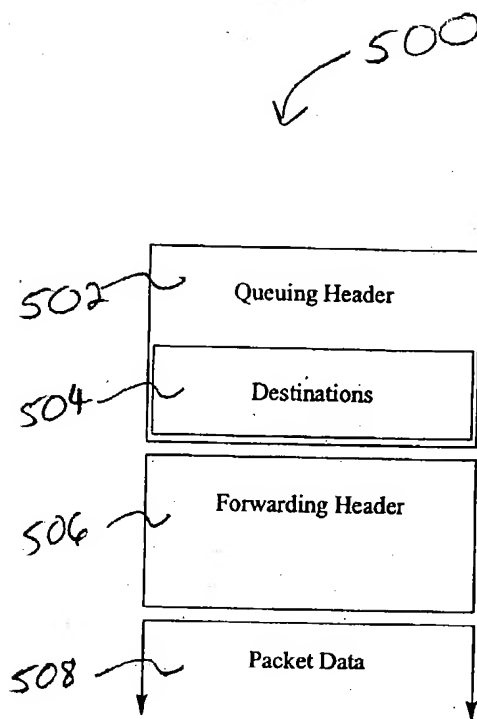
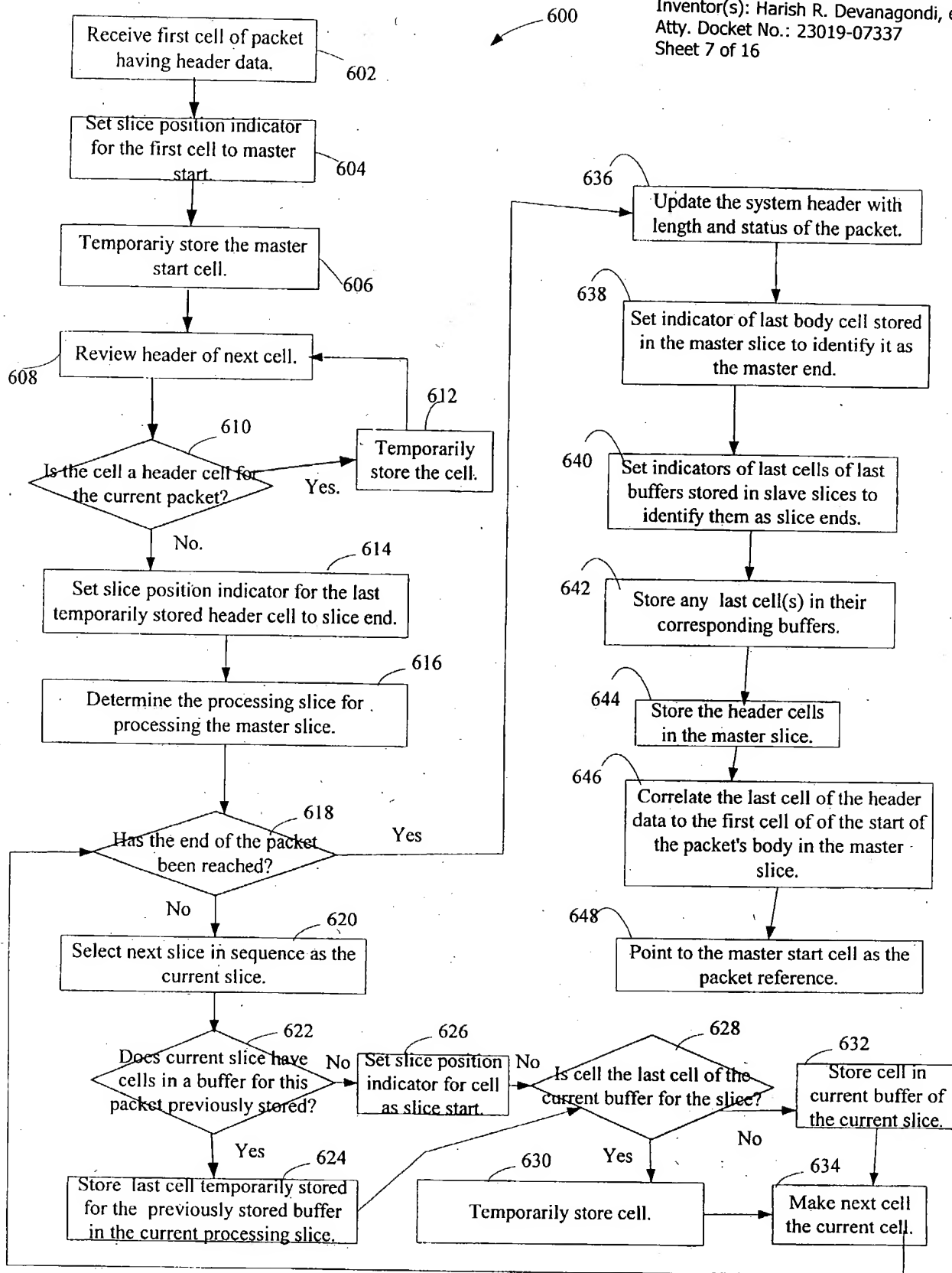


FIG. 4B







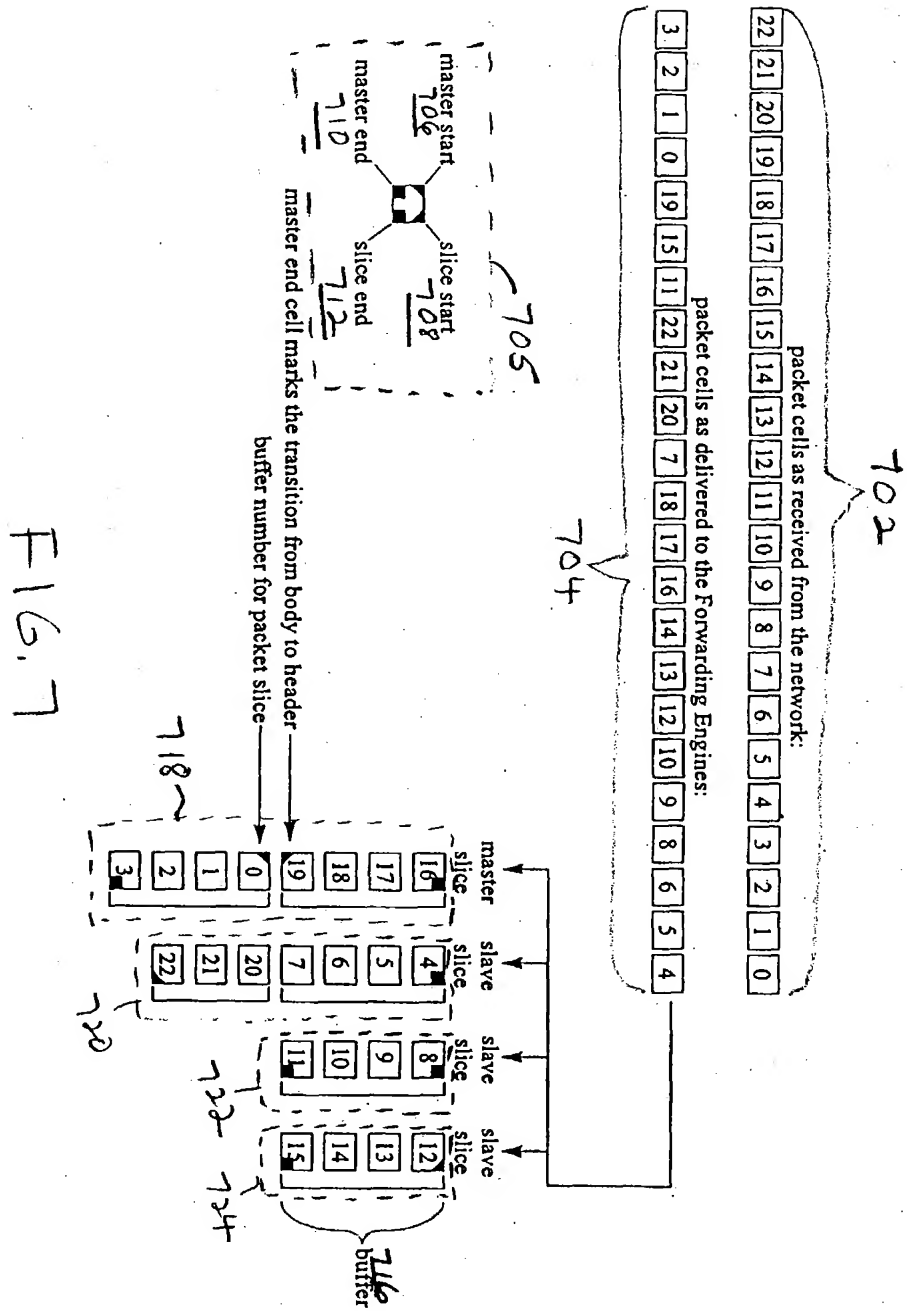
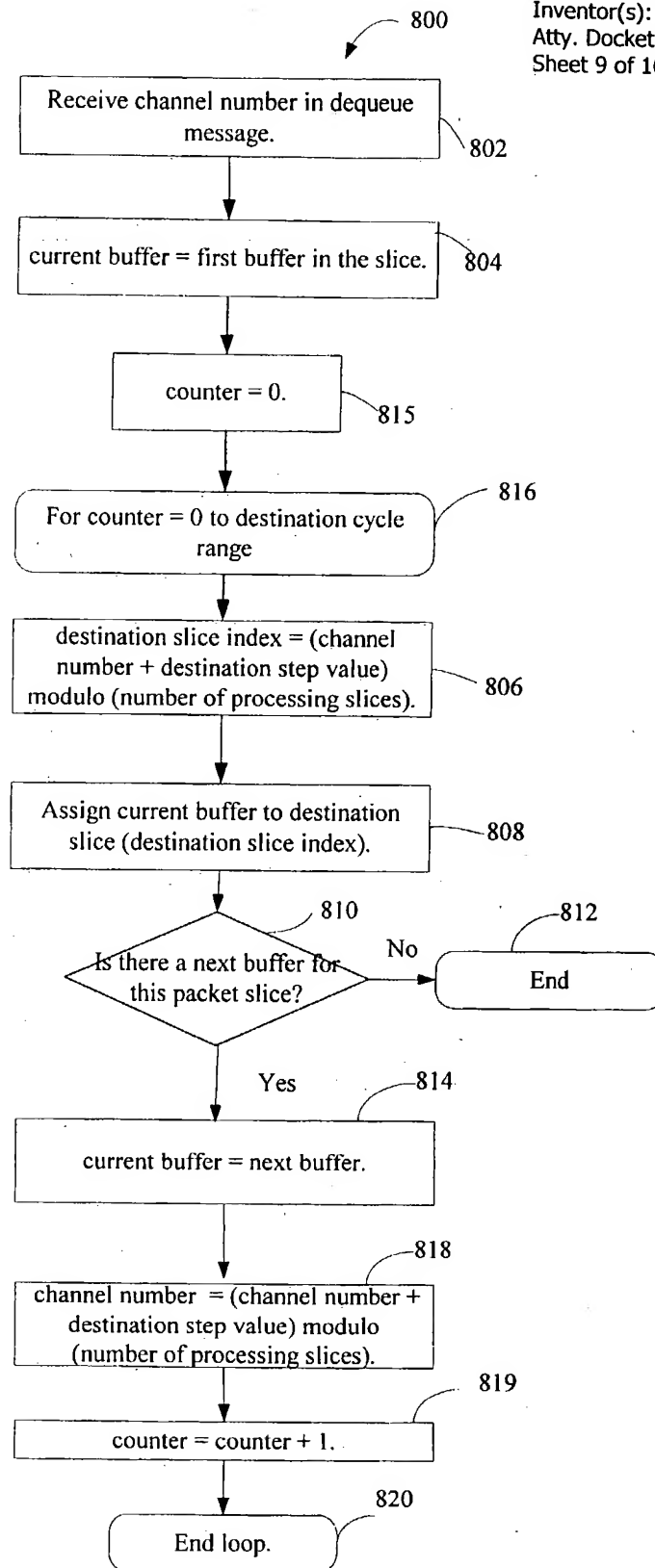
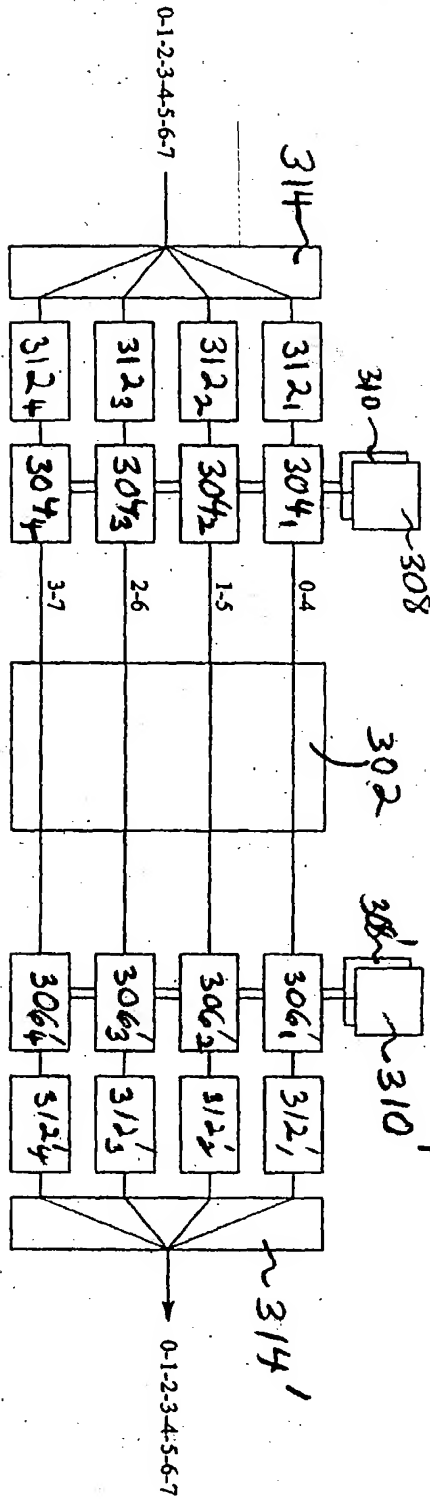


FIG. 8



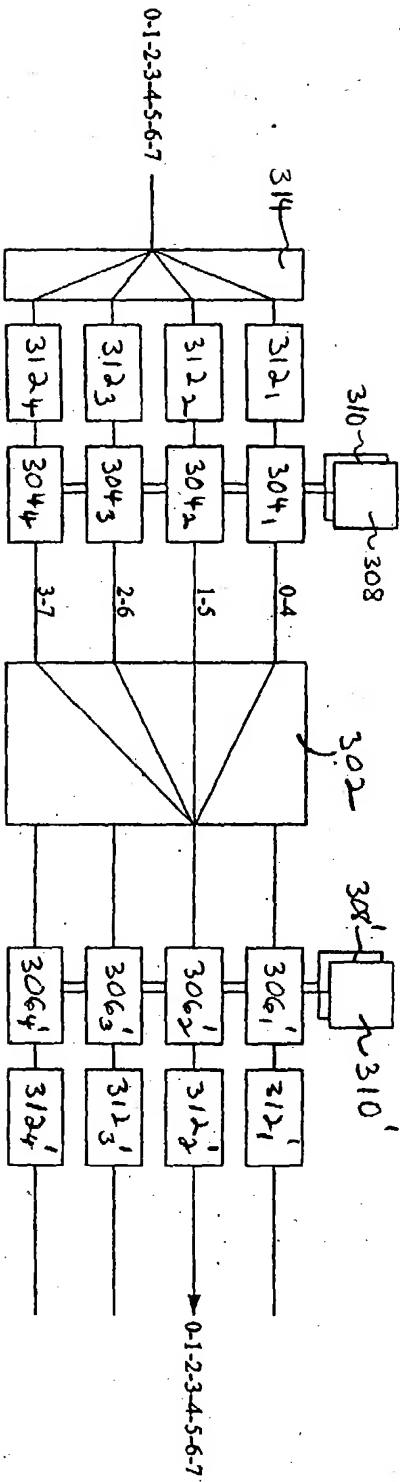
High Speed to High Speed Example

FIG. 9A



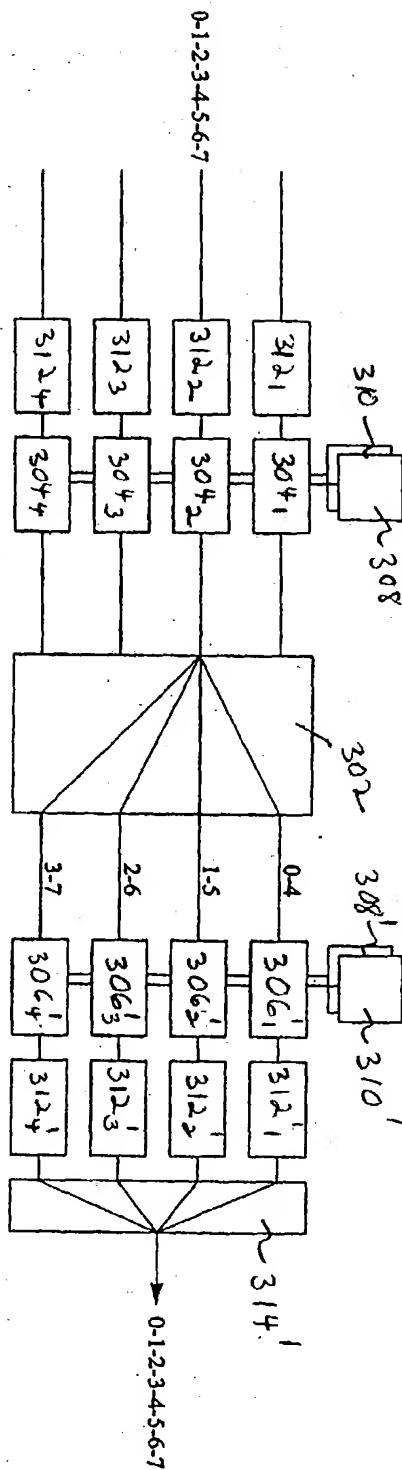
High Speed to Low Speed Example

FIG. 9B



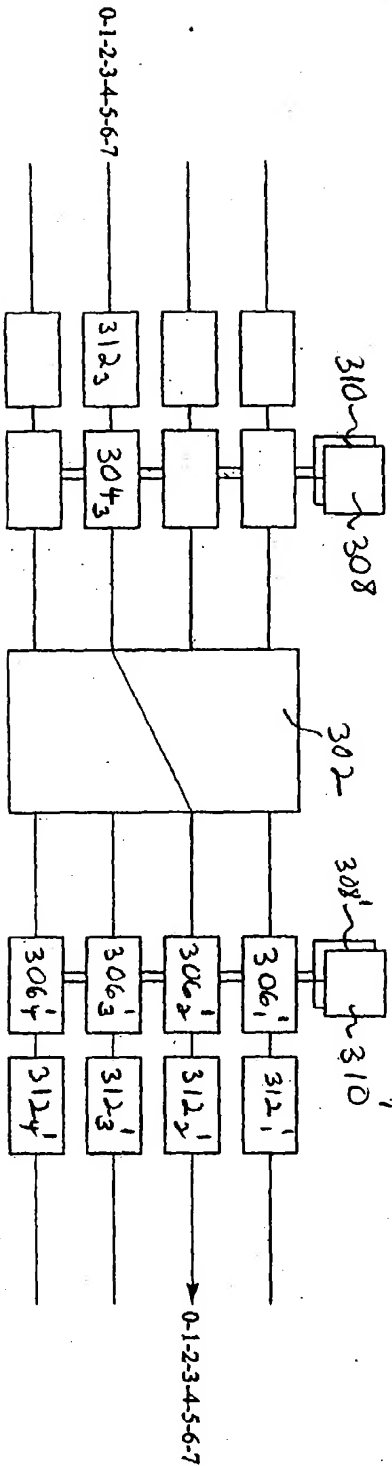
Low Speed to High Speed Example

F16:9C

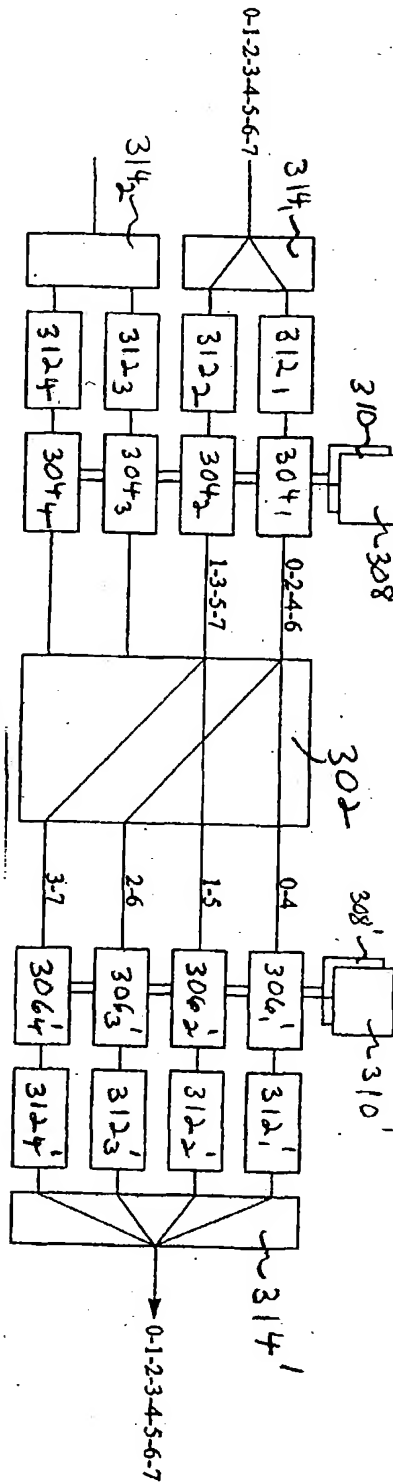


Low Speed to Low Speed Example

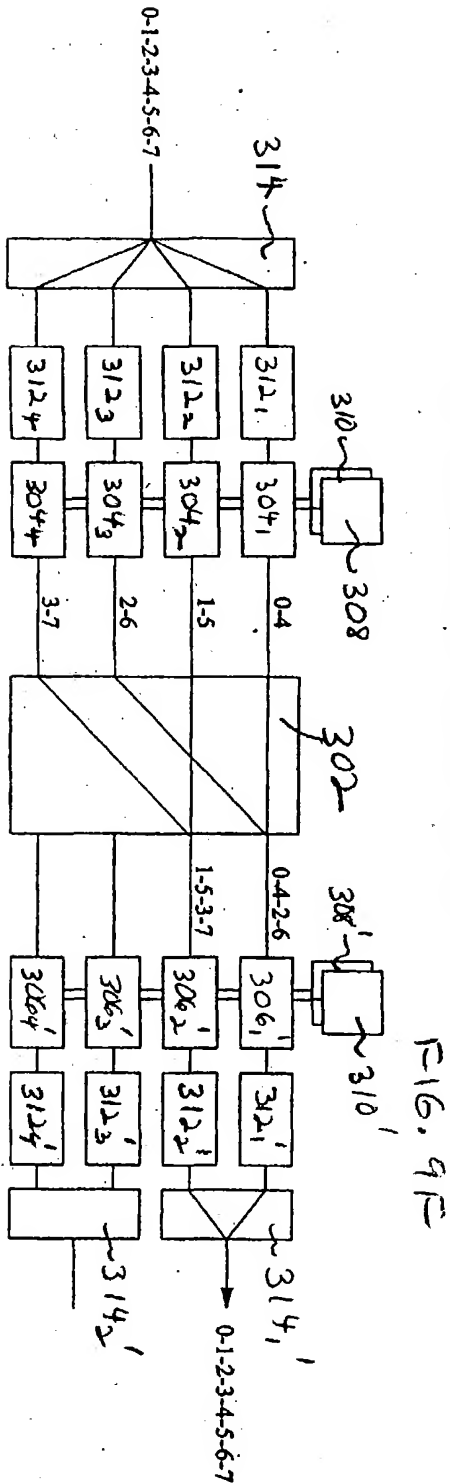
16. 97



Medium Speed to High Speed Example F16.9E



High Speed to Medium Speed Example



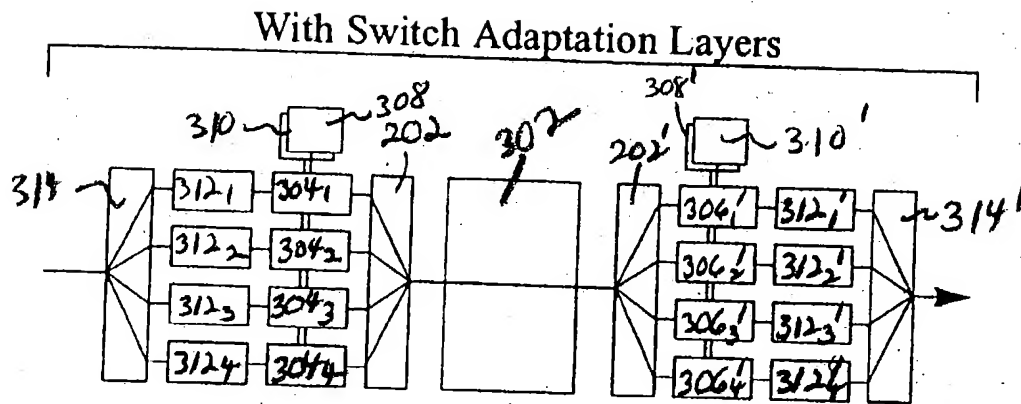


FIG. 9G

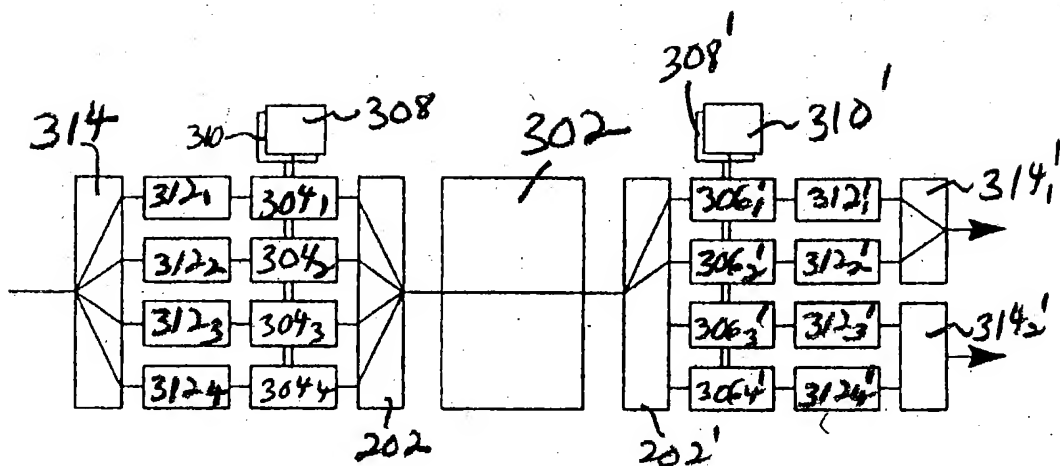
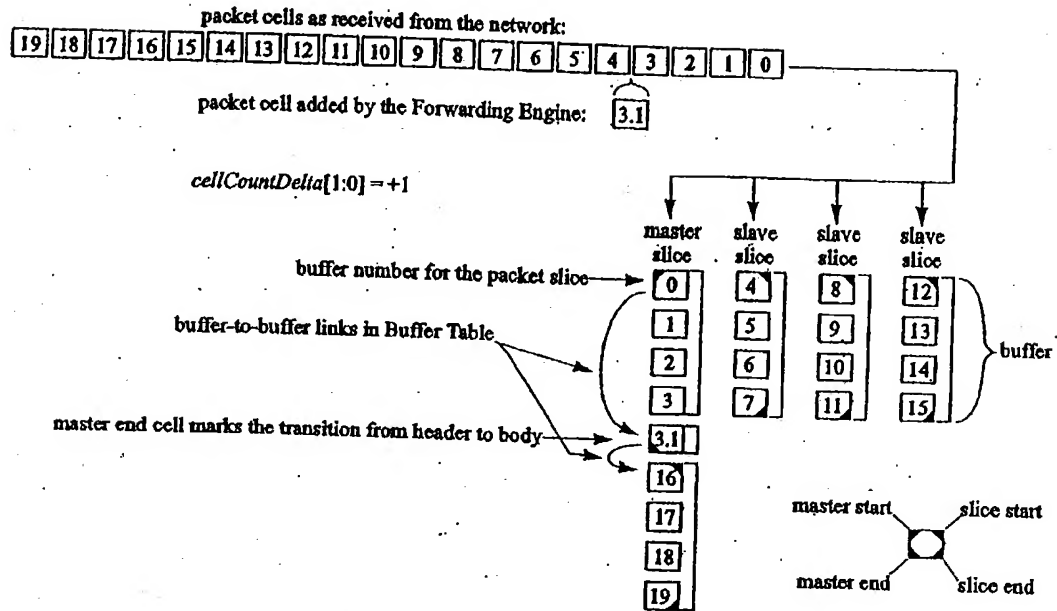


FIG. 9H

Header Expansion on Ingress Store

FIG. 10A



Header Expansion on Ingress Store with Post Storage Header Delivery

FIG. 10B

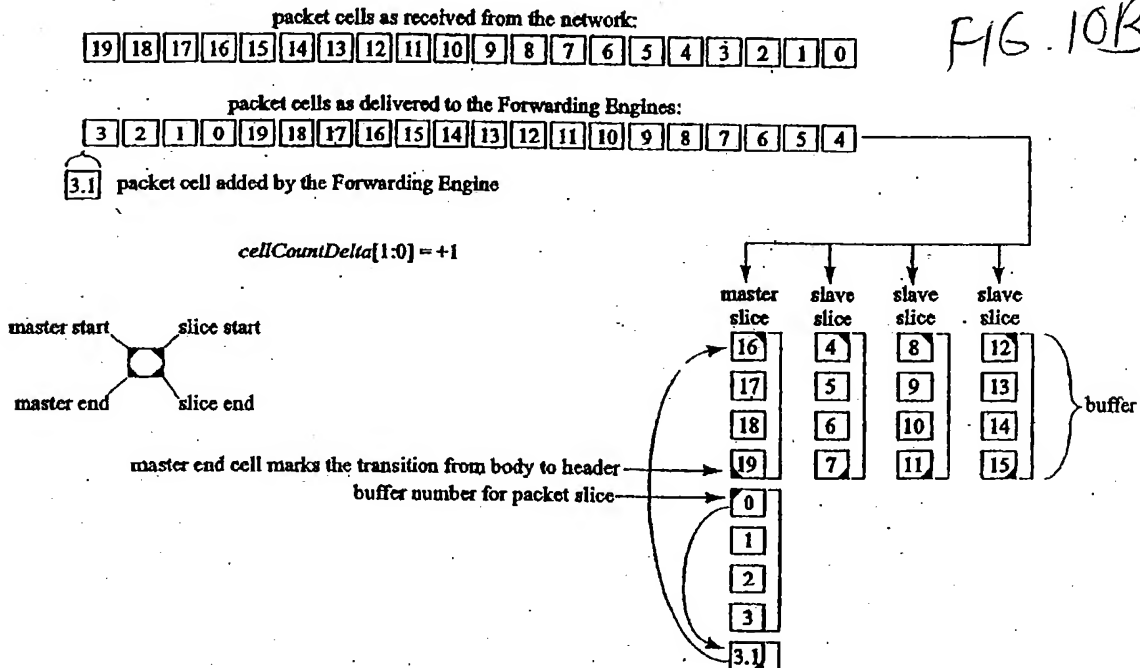


FIG. 10C

Cell Sequencing on Egress Store

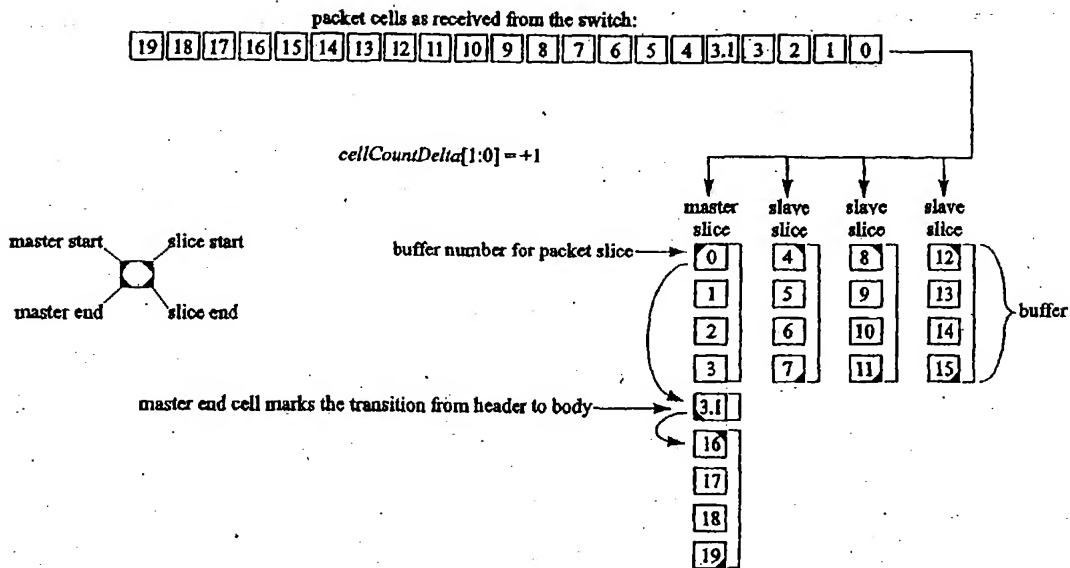


FIG. 10D

Cell Sequencing and Elasticity on Egress Retrieve

